



# User Guide

## EVB-ATEK252N3-03

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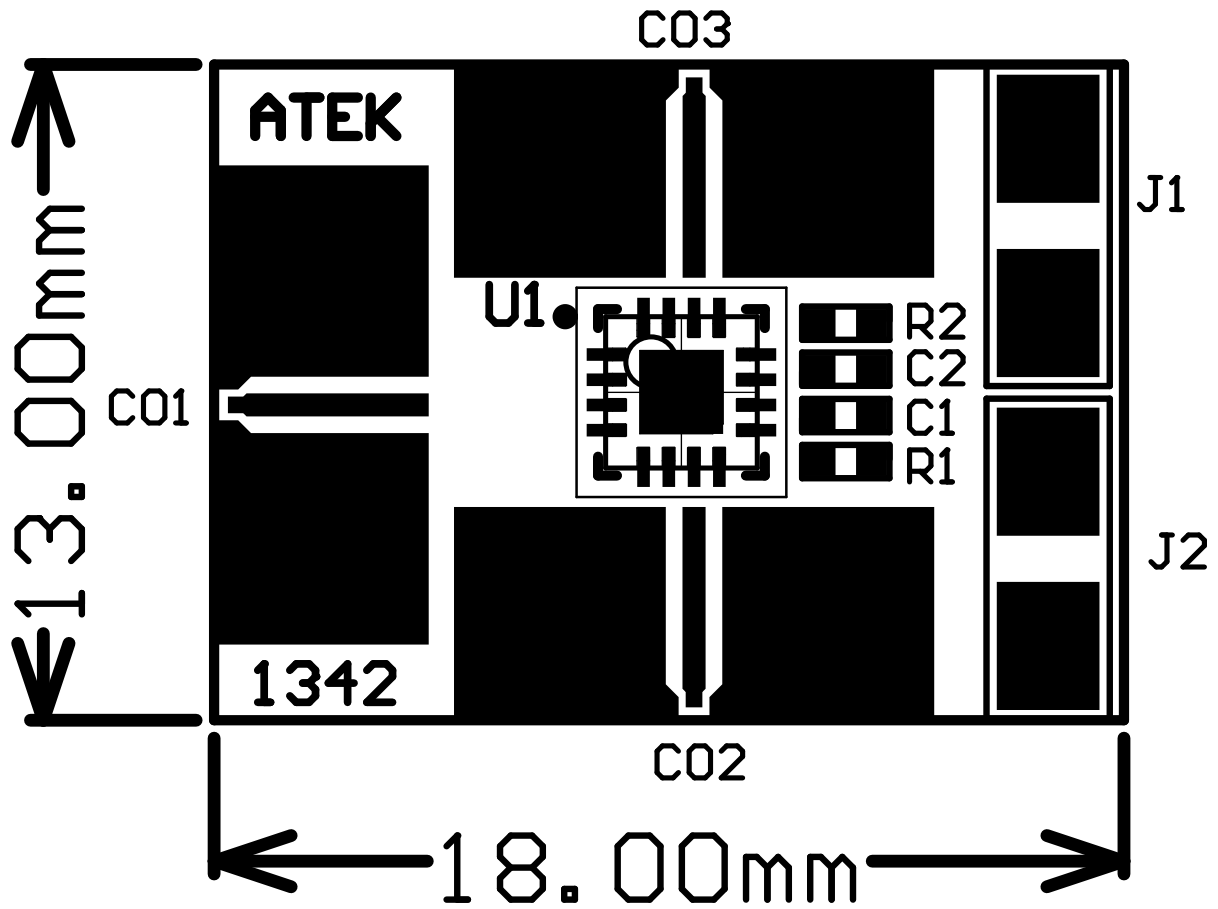
**Revisions**

Revision No	Revision Date	Revision Reason	Section / Page No
1.0	16.06.2022	Initial Release	

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1 GENERAL INFORMATION



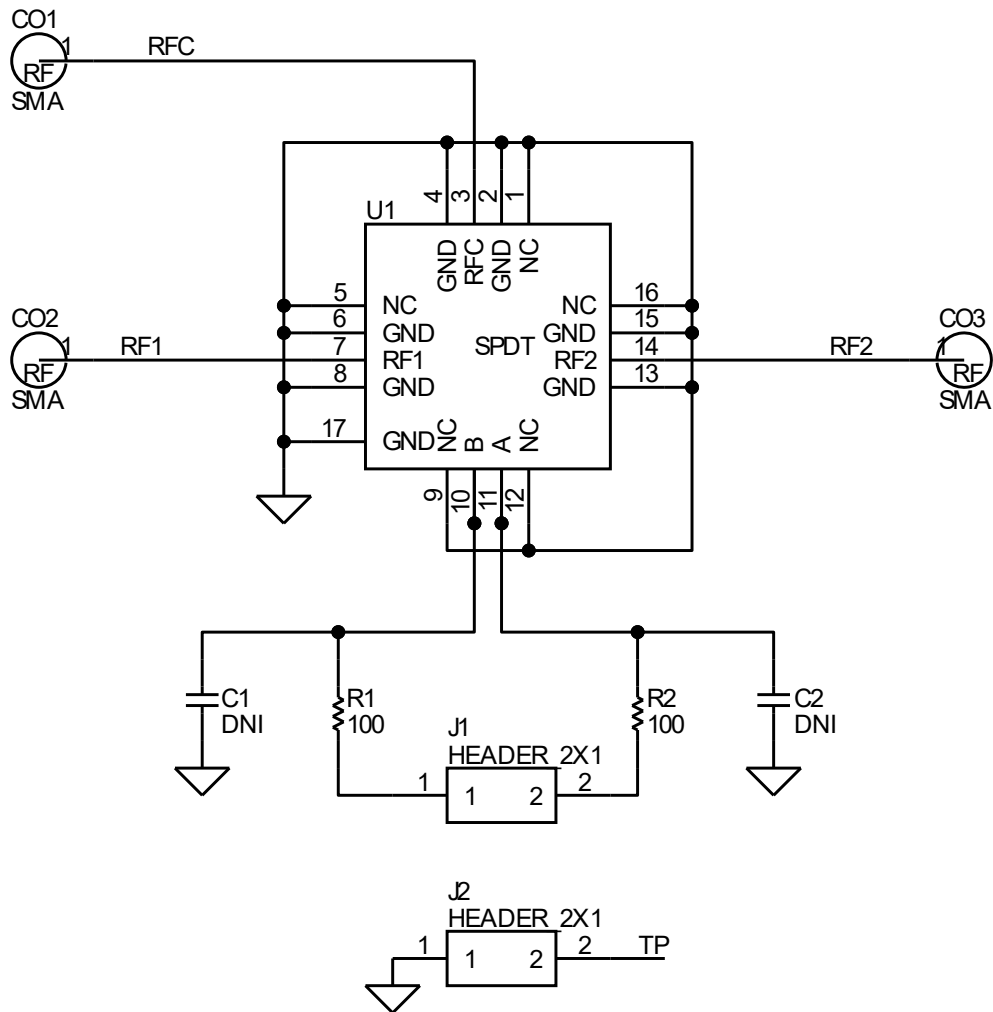
PIN Name	Definition	Comment
<b>C01</b>	RF IN	K Connector
<b>C02, C03</b>	RF OUT	K Connector
<b>J1 Up</b>	CTRL A	2.54mm Header
<b>J1 Down</b>	CTRL B	2.54mm Header
<b>J2 Up</b>	N/A	2.54mm Header
<b>J2 Down</b>	GND	2.54mm Header

Notes:

1. VDD Voltage is detailed in Datasheet.
2. Control Voltages are detailed in Datasheet.
3. The definition of up, down, right, and left is valid for this view of PCB.

2 DESIGN INFORMATION

2.1 SCHEMATIC



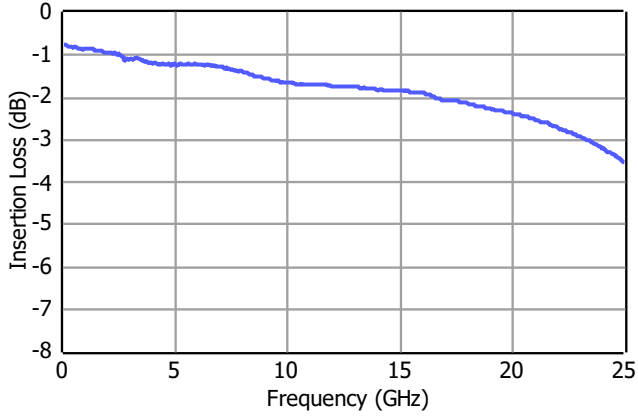
2.2 BOM

Designator	Footprint	Qty	Comment	PN
C1, C2	0402	2	DNP	
CO1, CO2, CO3	K Connector	3	K Connector	ATEK9292
J1, J2	2x1 Header	2	2x1 Header	
R1, R2	0402	2	OR	
U1	ATEKQ3316	1	SPDT	ATEK252N3

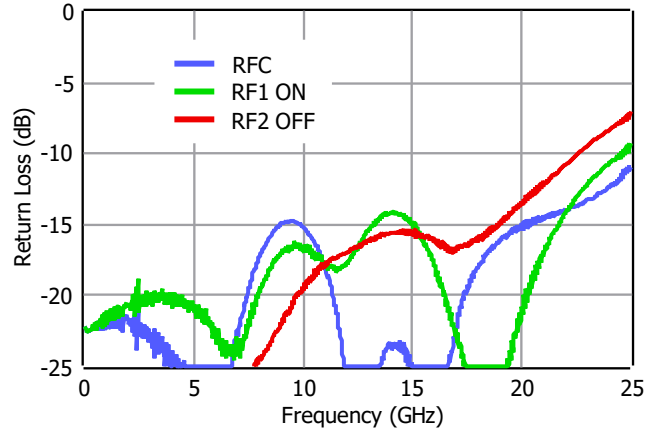
3 TYPICAL PERFORMANCE PLOTS

Conditions unless otherwise specified:  $V_{CTRL} = 0/-5\text{ V}$ ,  $T = 25\text{ C}$ , CW. For details, please refer to the datasheet.

Insertion Loss



Return Loss



Isolation from RFC to RF2

