ATEK MIDAS

Golden Touch on IC

User Guide
EVB-ATEK255P4-02

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Revisions

Revision No	Revision Date	Revision Reason	Section / Page No
1.0	10.07.2021	Initial Version	

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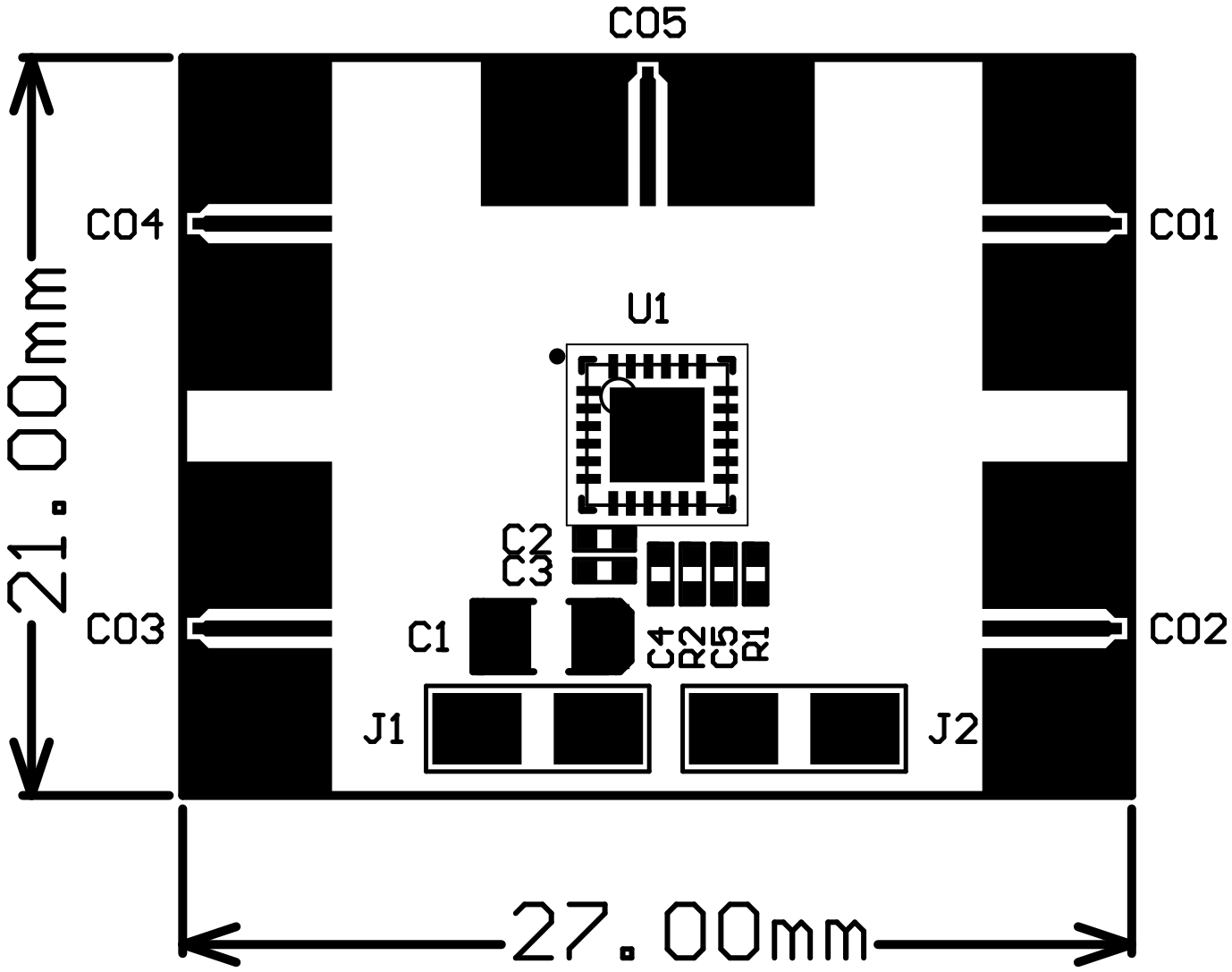
1 GENERAL INFORMATION



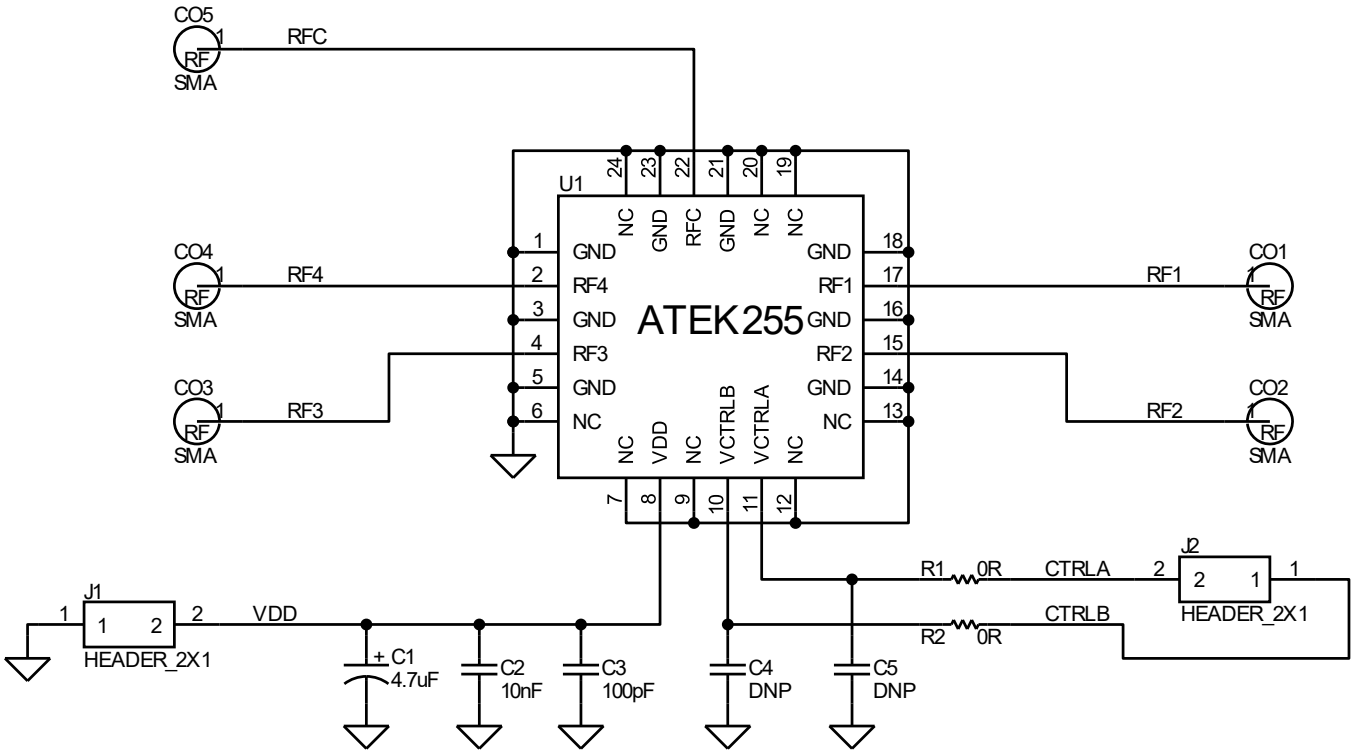
PIN Name	Definition	Comment
CO5	RF IN	K Connector
CO1, CO2, CO3, CO4	RF OUT	K Connector
J1 Right	VCC	2.54mm Header
J1 Left	GND	2.54mm Header
J2 Right	CTRLA	2.54mm Header
J2 Left	CTRLB	2.54mm Header

Notes:

1. VDD Voltage is detailed in Datasheet.
2. Control Voltage is detailed in Datasheet.
3. The definition of up, down, right, and left is valid for this view of PCB.

2 DESIGN INFORMATION

2.1 SCHEMATIC



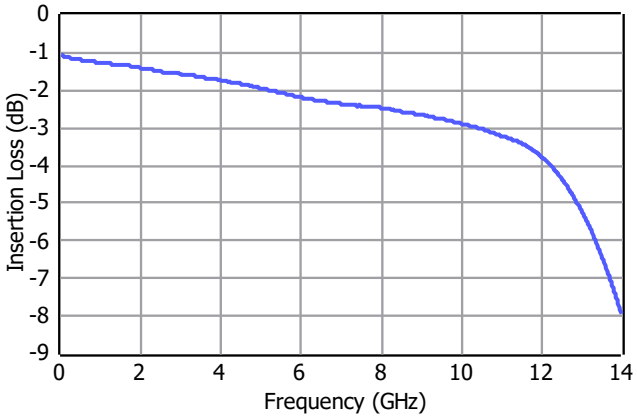
2.2 BOM

Designator	Footprint	Qty	Comment	PN
C1	CASEA	1	4.7 uF	
C2	0402	1	10 nF	
C3	0402	1	100 pF	
C4, C5	0402	2	DNP	
CO1, CO2, CO3, CO4, CO5	K Connector	5	K Connector	ATEK9292
J1, J2	HEADER_2X1	2	HEADER_2X1	
R1, R2	0402	2	0R	
U1	ATEKQ4424	1	SP4T	ATEK255 P54

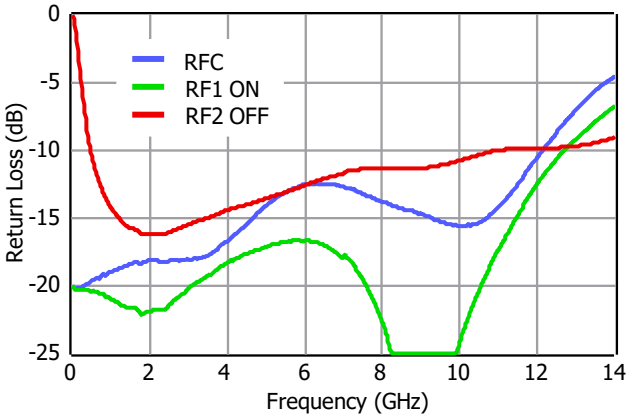
3 TYPICAL PERFORMANCE PLOTS

Conditions unless otherwise specified: V_{CTRL} =5 V, T = 25 C, CW. For details, please refer to the datasheet.

Insertion Loss



Return Loss



Isolation from RFC to RF2